



# DATA SHEET

## ARRAY CHIP RESISTORS YC/TC 124 (8Pin/4R) 5%, 1% sizes 4 × 0402

**RoHS** compliant





 Chip Resistor Surface Mount
 YC/TC
 SERIES
 124 (RoHS Compliant)

9

#### <u>SCOPE</u>

This specification describes YC124 (convex) and TC124 (concave) series chip resistor arrays with lead-free terminations made by thick film process.

#### **APPLICATIONS**

- Terminal for SDRAM and DDRAM
- Computer applications: laptop computer, desktop computer
- Consume electronic equipment: PDAs, PNDs
- Mobile phone, telecom...

#### **FEATURES**

- RoHS compliant
  - Products with lead free terminations meet RoHS requirements
  - Pb-glass contained in electrodes
  - Resistor element and glass are exempted by RoHS
- Reduce environmentally hazardous wastes
- High component and equipment reliability
- Save of PCB space
- None forbidden-materials used in products/production
- Halogen Free Epoxy

#### ORDERING INFORMATION - GLOBAL PART NUMBER & 12NC

Both part numbers are identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

#### YAGEO BRAND ordering code

#### GLOBAL PART NUMBER (PREFERRED)

YC	_	X	X	x	xx	xxxx	L	
тс		(I)	(2)	(3)	(4)	(5)	(6)	

#### (I) TOLERANCE

 $F = \pm 1\%$ 

 $J = \pm 5\%$  (for Jumper ordering, use code of J)

#### (2) PACKAGING TYPE

R = Paper taping reel

#### (3) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Based on spec

#### (4) TAPING REEL

- 07 = 7 inch dia. Reel
- 10 = 10 inch dia. Reel
- 13 = 13 inch dia. Reel

#### (5) RESISTANCE VALUE

There are  $2\sim4$  digits indicated the resistor value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g. IK2, not IK20.

Detailed resistance rules show in table of "Resistance rule of global part number".

#### (6) DEFAULT CODE

Letter L is the system default code for ordering only. (Note)

Resistance rule of global part number					
Resistance code ru	le Example				
0R	0R = Jumper				
XRXX (Ι to 9.76 Ω)	IR = Ι Ω IR5 = Ι.5 Ω 9R76 = 9.76 Ω				
XXRX (10 to 97.6 Ω)	IOR = 10 Ω 97R6 = 97.6 Ω				
XXXR (100 to 976 Ω <b>)</b>	100R = 100 Ω				
XKXX (1 to 9.76 K <b>Ω)</b>	IK = 1,000 Ω 9K76 = 9760 Ω				
XMXX (1 to 9.76 M <b>Ω)</b>	$IM = I,000,000 \Omega$ 9M76= 9,760,000 $\Omega$				

#### **ORDERING EXAMPLE**

The ordering code of a YCI24 convex chip resistor array, value 1,000  $\Omega$  with ±5% tolerance, supplied in 7-inch tape reel is: YCI24-JR-071KL.

#### NOTE

- All our RSMD products meet RoHS compliant. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- On customized label, "LFP" or specific symbol printed and the optional "L" at the end of GLOBAL PART NUMBER / I2NC can be added (both are on customer request)

3

#### PHYCOMP BRAND ordering codes

Both GLOBAL PART NUMBER (preferred) and I2NC (traditional) codes are acceptable to order Phycomp brand products.

#### **GLOBAL PART NUMBER** (PREFERRED)

For detailed information of GLOBAL PART NUMBER and ordering example, please refer to page 2. TCI24 series is supplied and ordered by global part number only.

#### 12NC CODE

2350 <u>XXX XX</u> XXX L					Last di	git of I2N	С		
(1)	1	(	2) (3) (4)			Resistance	decade <sup>(3</sup>	)	Last digit
TYPE/	START	tol. resistance		PAPER / PE TAPE	PAPER / PE TAPE ON REEL (units) <sup>(2)</sup>		)976 Ω		0
4×0402	IN <sup>(1)</sup>	(%)	RANGE	10,000	40,000	0.1 to 0.97	76 Ω		7
ARV341	2350	±5%	l to I MΩ	033   I xxx	033   3xxx	l to 9.76 9	Ω		8
ARV342	2350	±1%	I to I MΩ	023 2xxxx	023 8xxxx	10 to 97.6	Ω		9
Jumper	2350	-	0 Ω	033 91001	-	100 to 976	δΩ		I
(I) The	resisto	rs have	a 12-digit ord	ering code starting	with 2350.	l to 9.76 l	<Ω		2
			•			10 to 97.6	KΩ		3
. ,	e subsequ kaging.	Jent 4	or 5 algits indi	cate the resistor to	pierance and	100 to 976	6 ΚΩ		4
		1 -				l to 9.76 l	MΩ		5
. ,		•	• ·	sent the resistance as shown in the ta		10 to 97.6	MΩ		6
	st digit c	-				Example:	0.02 Ω	=	0200 or 200
(4) "L"	is optior	nal sym	nbol <sup>(Note)</sup> .				0.3 Ω	=	3007 or 307
Order	ORDERING EXAMPLE						ΙΩ	=	1008 or 108
The ordering code of a ARV341 resistor, value 1,000 $\Omega$ with ±5%							33 KΩ	=	3303 or 333
toleran	-	lied in	tape of 10,000	units per reel is: 2			10 MΩ	=	1006 or 106

#### ΝΟΤΕ

- I. All our RSMD products are RoHS compliant. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- 2. On customized label, "LFP" or specific symbol printed and the optional "L" at the end of GLOBAL PART NUMBER / 12NC can be added (both are on customer request)

	Chip Resistor Su	rface Mount	YC/TC	SERIES	124 (RoHS Compliant)	9
<u>MARKING</u>						
YC124						
	O  mper = 0 Ω	I-Digit markir	ng			
	<b>24</b> alue = 240 KΩ	E-24 series: 3 First two digit		ificant fiş	gure and 3rd digit for number of zeros	
TCI24						
		No marking				

For further marking information, please refer to data sheet "Chip resistors marking".

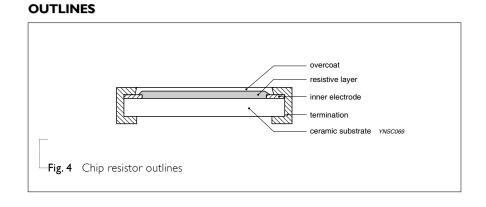


YAGEO Phicomp

Product specification 4

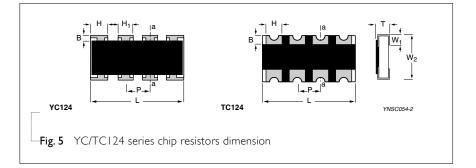
#### **CONSTRUCTION**

The resistor is constructed on top of a high-grade ceramic body. Internal metal electrodes are added on each end to make the contacts to the thick film resistive element. The composition of the resistive element is a noble metal imbedded into a glass and covered by a second glass to prevent environment influences. The resistor is laser trimmed to the rated resistance value. The resistor is covered with a protective epoxy coat, finally the two external terminations (matte tin on Ni-barrier) are added, as shown in Fig 4.

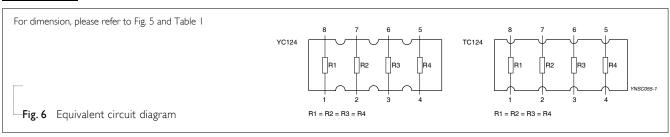


#### **DIMENSIONS**

Table I		
TYPE	YCI24	TCI24
B (mm)	0.20 ±0.15	0.20 ±0.10
H (mm)	0.45 ±0.05	0.30 ±0.10
H <sub>I</sub> (mm)	0.30 ±0.05	
P (mm)	0.50 ±0.05	0.50 ±0.05
L (mm)	2.00 ±0.10	2.00 ±0.10
T (mm)	0.45 ±0.10	0.40 ±0.10
W <sub>I</sub> (mm)	0.30 ±0.15	0.25 ±0.10
W <sub>2</sub> (mm)	1.00 ±0.10	1.00 ±0.10



#### **SCHEMATIC**



Chip Resistor Surface Mount YC/TC SERIES 124 (RoHS Compliant)

ELECTRICAL CHARACTERISTICS

Table 2			
CHARACTERISTICS		YCI24	TCI24
Operating Temperature Range	-55	°C to +155 °C	–55 °C to +125 °C
Rated Power		1/16 W	1/16 W
Maximum Working Voltage		25 V	50 V
Maximum Overload Voltage		50 V	100 V
Dielectric Withstanding Voltage		100 V	100 V
Resistance Range	5% (E24) 1% (E24/E96)	$\mid \Omega$ to $\mid M\Omega$	0 Ω to   MΩ
		Zero O	hm Jumper < 0.05 $\Omega$
Temperature Coefficient	$  \Omega \leq R <  0 \Omega$	±250 ppm/°C	1200 /80
	$ 0 \Omega \leq R \leq   M\Omega$	±200 ppm/°C	±200 ppm/°C
Jumper Criteria	Rated Current	1.0 A	1.0 A
	Maximum Current	2.0 A	1.5 A

#### FOOTPRINT AND SOLDERING PROFILES

Recommended footprint and soldering profiles, please refer to data sheet "Chip resistors mounting".

#### PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity						
PRODUCT TYPE	PACKING STYLE	REEL DIMENSION	QUANTITY PER REEL			
YC/TCI24	Paper Taping Reel (R)	7" (178 mm)	10,000 units			
		10" (254 mm)	20,000 units			
		13" (330 mm)	40,000 units			

#### NOTE

1. For paper tape and reel specification/dimensions, please refer to data sheet "Chip resistors packing".

#### FUNCTIONAL DESCRIPTION

#### **OPERATING TEMPERATURE RANGE**

- YCI24: -55 °C to +155 °C
- TCI24: -55 °C to +125 °C

#### **POWER RATING**

YC/TC 124 rated power at 70 °C is 1/16 W

#### **R**ATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

 $V=\sqrt{(P \times R)}$  or max. working voltage whichever is less

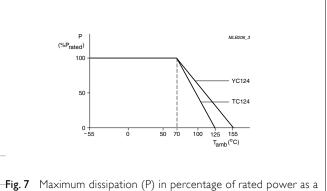
#### Where

```
V=Continuous rated DC or
AC (rms) working voltage (V)
```

P=Rated power (W)

R=Resistance value ( $\Omega$ )





function of the operating ambient temperature  $(T_{amb})$ 

 Chip Resistor Surface Mount
 YC/TC
 SERIES
 124 (RoHS Compliant)

7 9

#### TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Life/	MIL-STD-202G-method 108A	1,000 hours at 70±5 °C applied RCWV	±(2%+0.05 Ω)
Operational	IEC 60115-1 4.25.1	1.5 hours on, 0.5 hour off, still air required	<100 m $\Omega$ for Jumper
Life/ Endurance	JIS C 5202-7.10		
High	MIL-STD-202G-method 108A	1,000 hours at maximum operating temperature	±(1%+0.05 Ω)
Temperature Exposure/	IEC 60115-1 4.25.3	depending on specification, unpowered	$<$ 50 m $\Omega$ for Jumper
Exposure/ Endurance at upper category	JIS C 5202-7.11	No direct impingement of forced air to the parts	
temperature		Tolerances: 155±3 °C	
Moisture	MIL-STD-202G-method 106F	Each temperature / humidity cycle is defined at	±(2%+0.05 Ω)
Resistance	IEC 60115-1 4.24.2	8 hours (method 106F), 3 cycles / 24 hours for 10d with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered	<100 m $\Omega$ for Jumper
		Parts mounted on test-boards, without condensation on parts	
		Measurement at 24±2 hours after test conclusion	
Thermal Shock	MIL-STD-202G-method 107G	YCI24: -55/+155 °C TCI24: -55/+125 °C	±(0.5%+0.05 Ω) for 10 KΩ to 10
		Note: Number of cycles required is 300. Devices unmounted	$\pm(1\%+0.05 \ \Omega)$ for others <50 mΩ for Jumper
		Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	
Short time	MIL-R-55342D-para 4.7.5	2.5 times RCWV or maximum overload voltage	±(2%+0.05 Ω)
overload	IEC60115-14.13	whichever is less for 5 sec at room temperature	$<$ 50 m $\Omega$ for Jumper
			No visible damage
Board Flex/	IEC60115-1 4.33	Device mounted on PCB test board as	±(1%+0.05 Ω)
Bending		described, only I board bending required	$<$ 50 m $\Omega$ for Jumper
		3 mm bending	No visible damage
		Bending time: 60±5 seconds	
		Ohmic value checked during bending	

Chip Resistor Surface Mount YC/TC SERIES 124 (RoHS Compliant)

Product specification 8 9

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability - Wetting	IPC/JEDECJ-STD-002B test B IEC 60068-2-58	Electrical Test not required Magnification 50X SMD conditions: I <sup>st</sup> step: method B, aging 4 hours at 155 °C dry heat 2 <sup>nd</sup> step: leadfree solder bath at 245±3 °C Dipping time: 3±0.5 seconds	Well tinned (≥95% covered) No visible damage
- Leaching	IPC/JEDECJ-STD-002B test D IEC 60068-2-58	Leadfree solder, 260 °C, 30 seconds immersion time	No visible damage
- Resistance to Soldering Heat	MIL-STD-202G-method 210F IEC 60068-2-58	Condition B, no pre-heat of samples Leadfree solder, 270 °C, 10 seconds immersion time Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	±(1%+0.05 Ω) <50 mΩ for Jumper No visible damage

 Chip Resistor Surface Mount
 YC/TC
 SERIES
 124 (RoHS Compliant)

<u>REVISION HISTORY</u>

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 3	Mar 09, 2011	-	- YCI24 resistance range extended
Version 2	Oct 29, 2008	-	<ul> <li>Change to dual brand datasheet that describes YC/TC124 with RoHS compliant</li> </ul>
			- Range extended to size TC124 (concave)
			- Description of "Halogen Free Epoxy" added
			- Define global part number
Version I	Feb 04, 2005	-	- New datasheet for 4 × 0402 chip resistor arrays 1% and 5% with lead- free terminations
			<ul> <li>Replace the 4 × 0402 part of pdf files: ARV341_5_PbFree_L_0.pdf and ARV342_1_PbFree_L_0.pdf</li> </ul>
			- Test method and procedure updated
Version 0	Dec 05, 2003	-	-

"Yageo reserves all the rights for revising the content of this datasheet without further notification, as long as the products itself are unchanged. Any product change will be announced by PCN."



9

# **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Yageo: YC124-JR-0733RL